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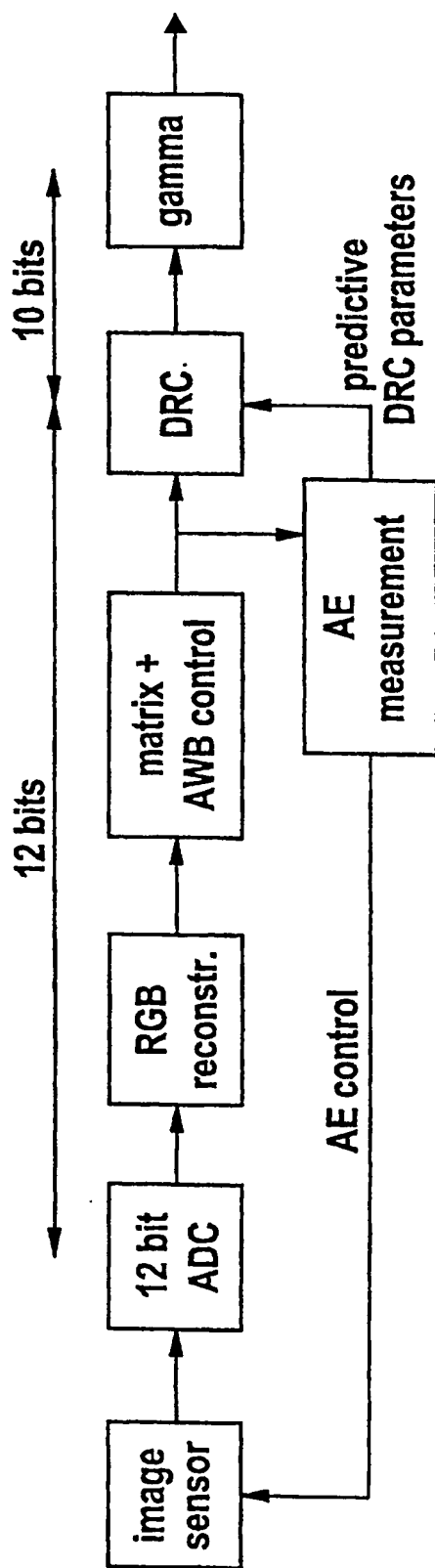


FIG.1

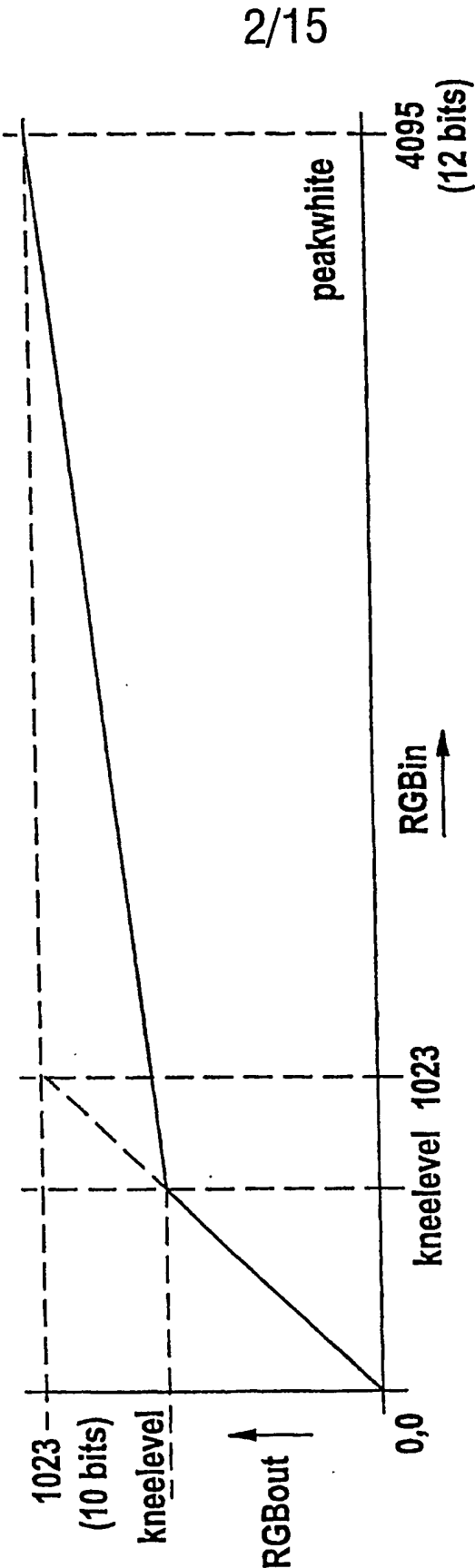


FIG.2

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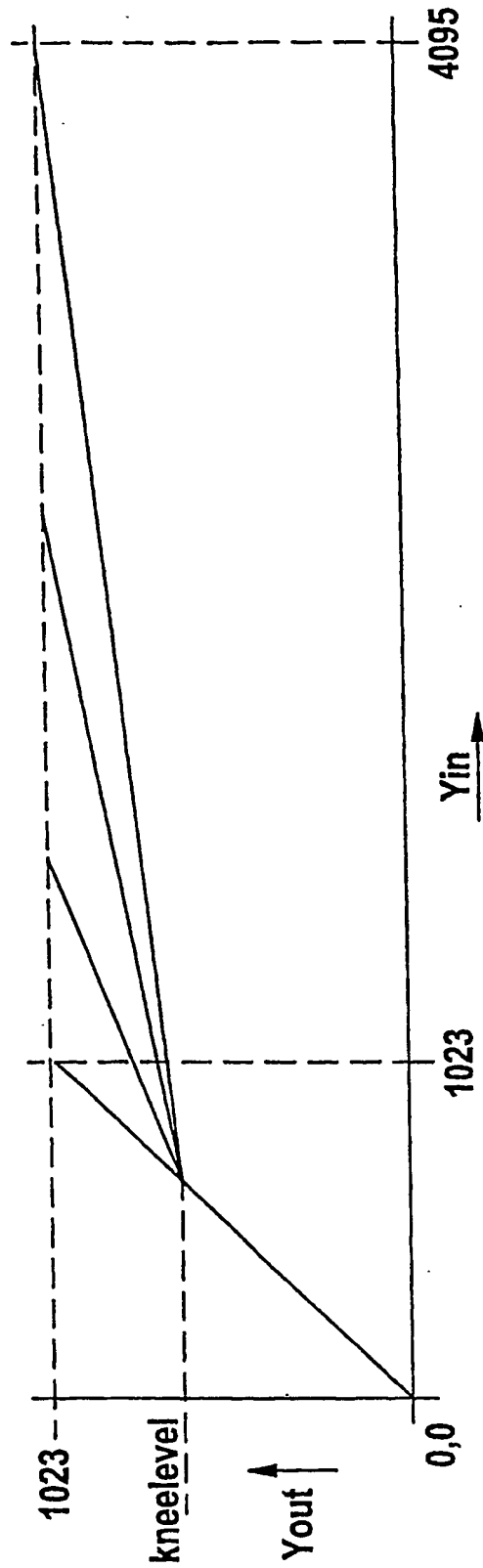


FIG.3

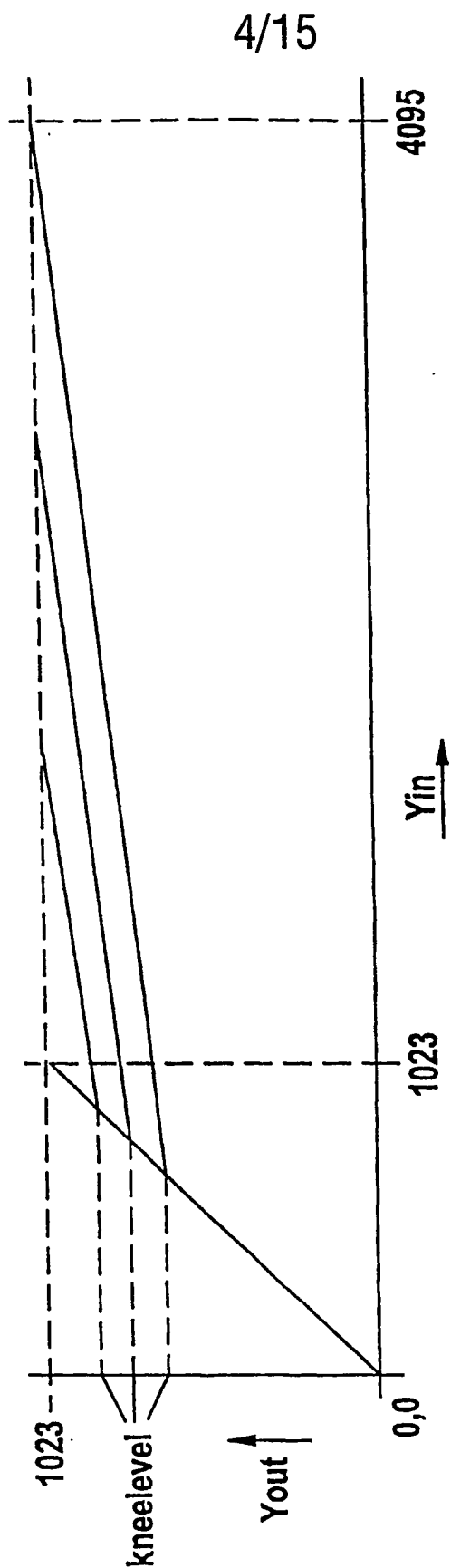


FIG.4

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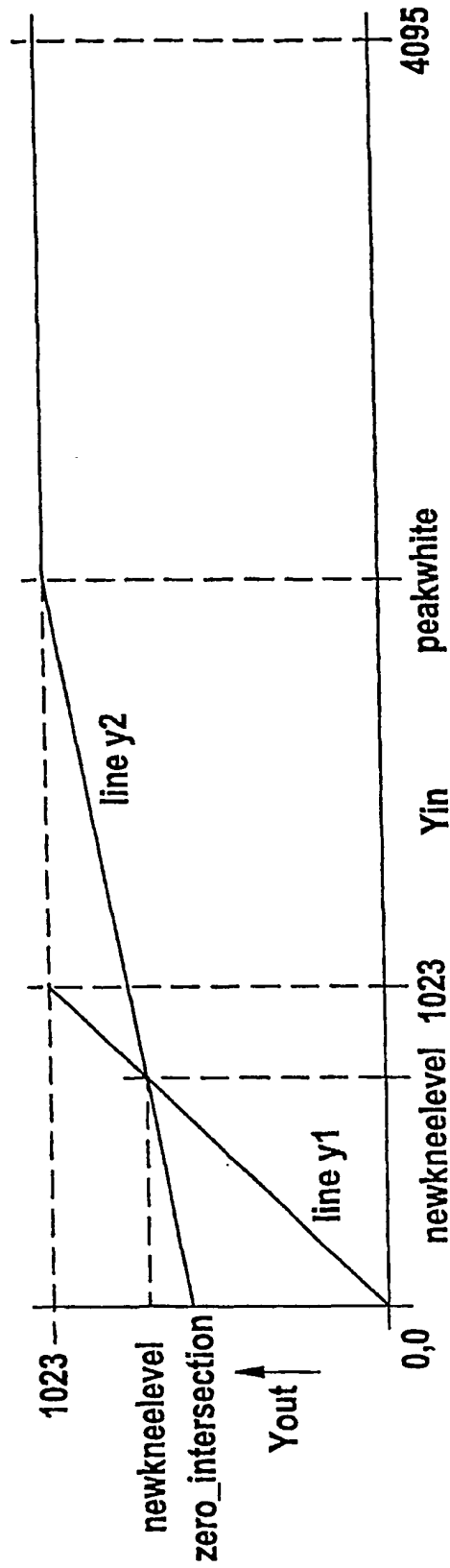


FIG.5

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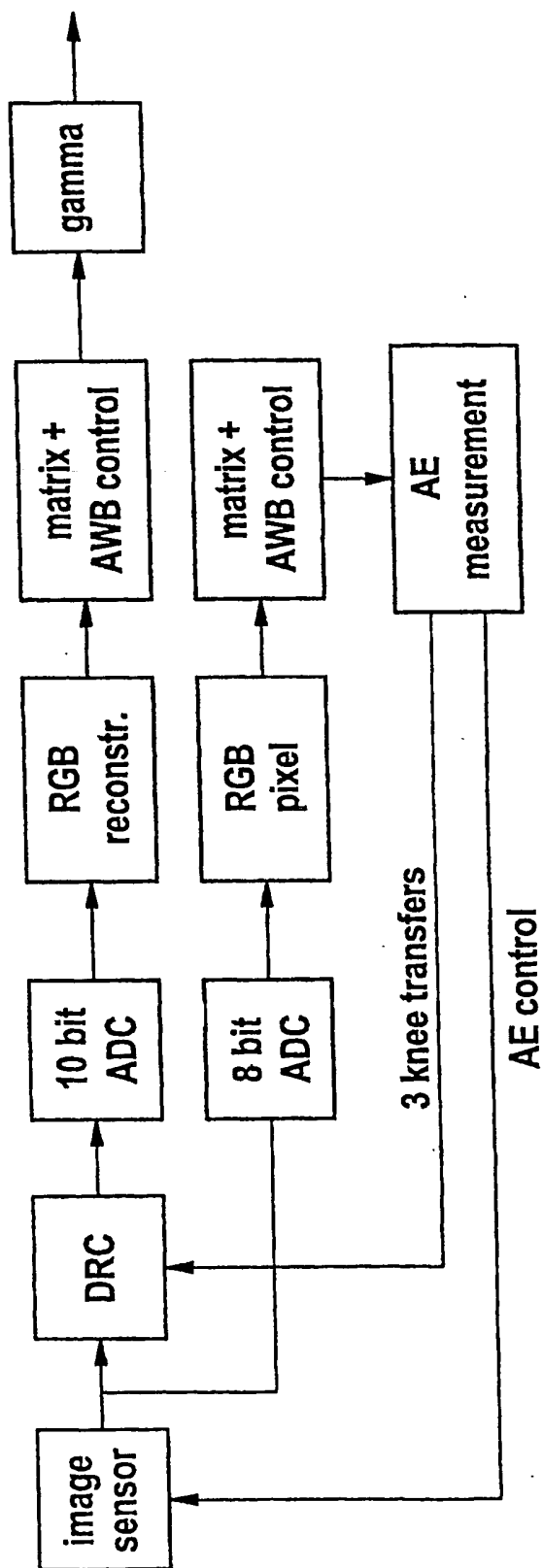


FIG.6

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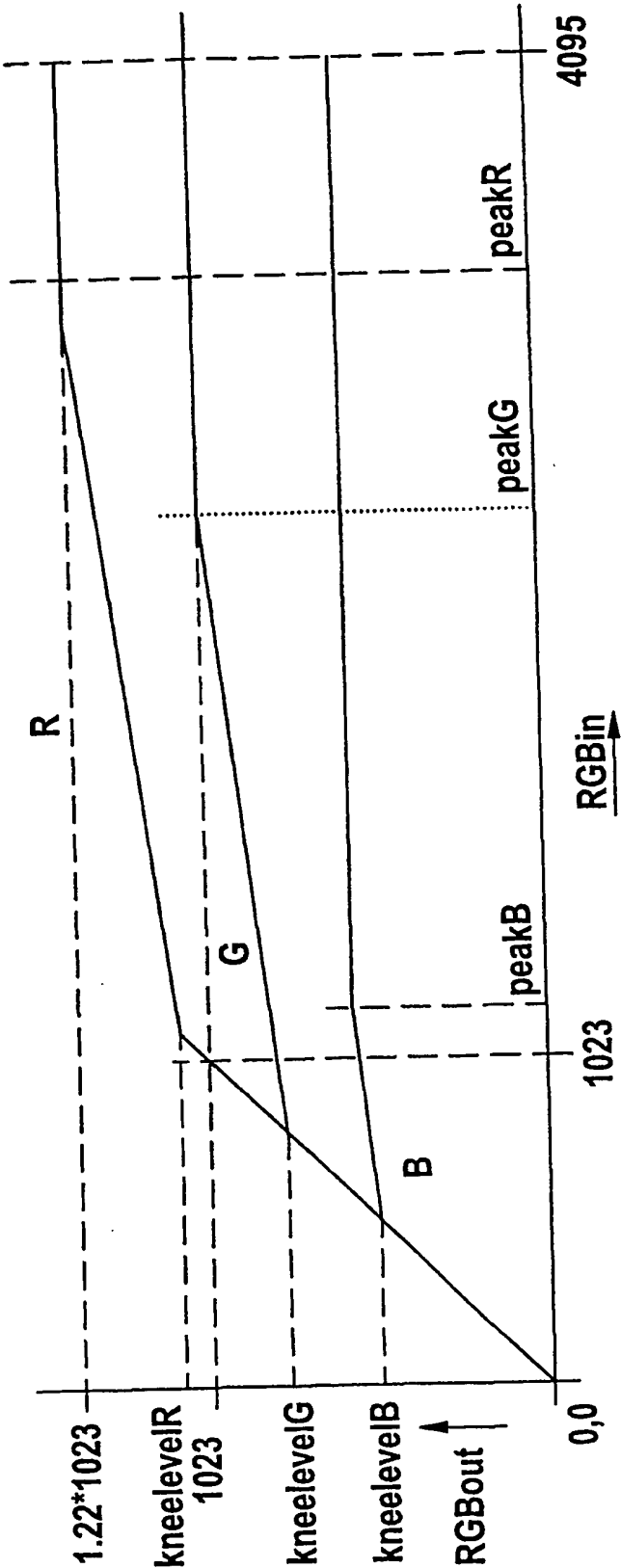


FIG.7

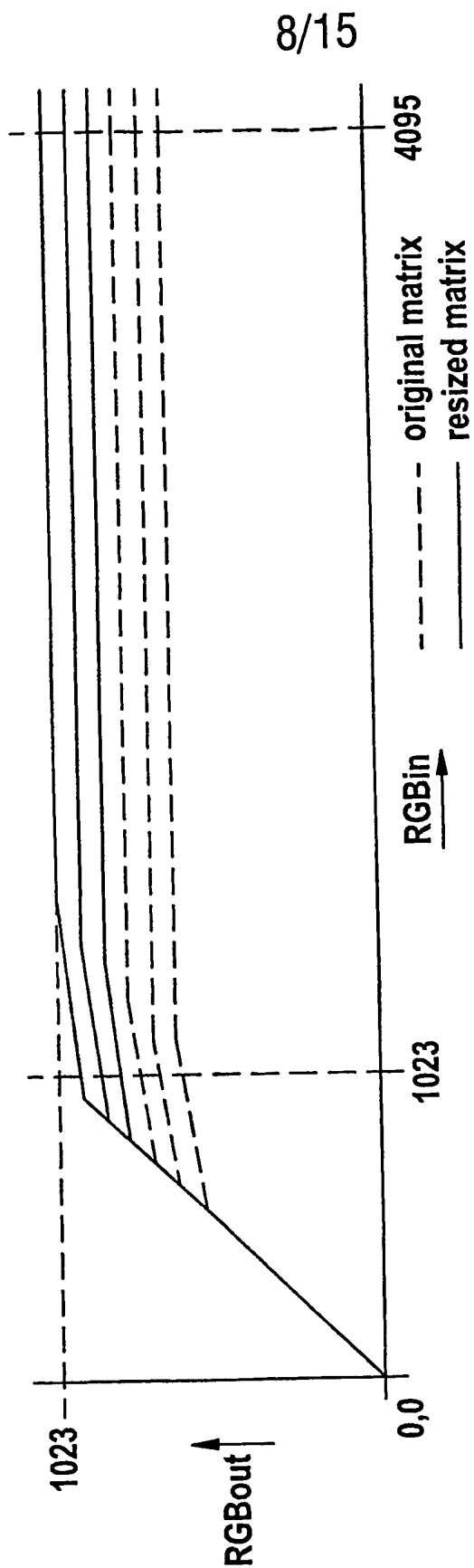


FIG.8



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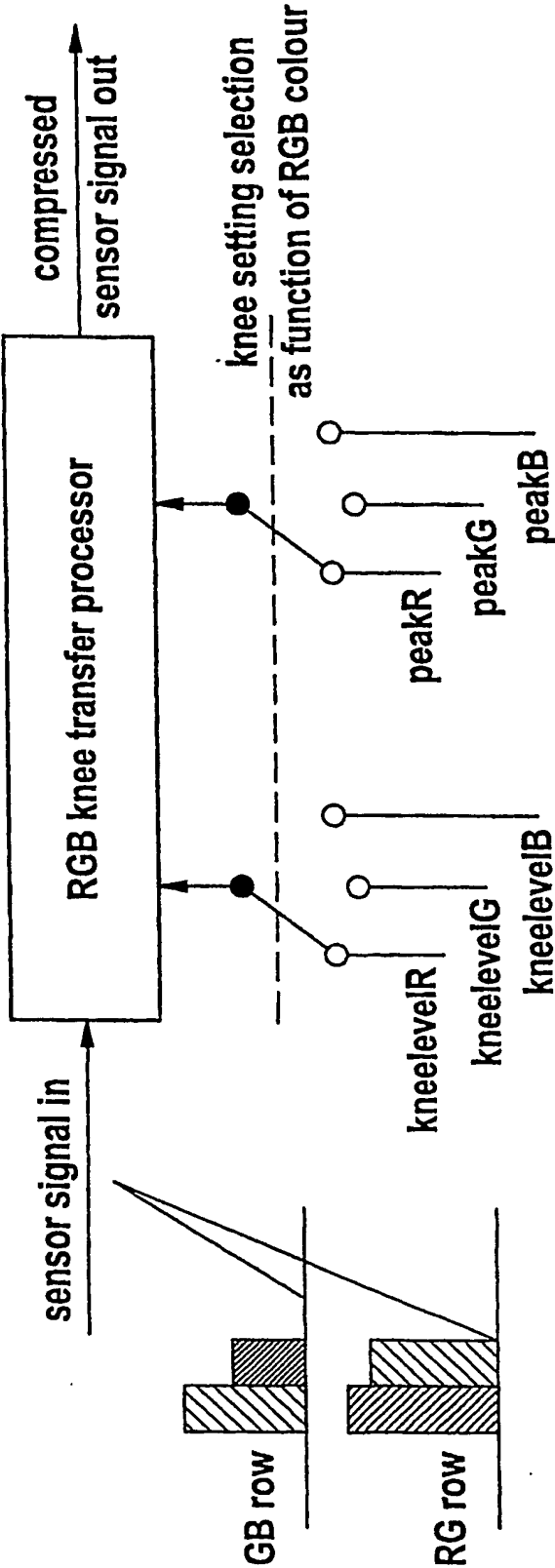


FIG.9

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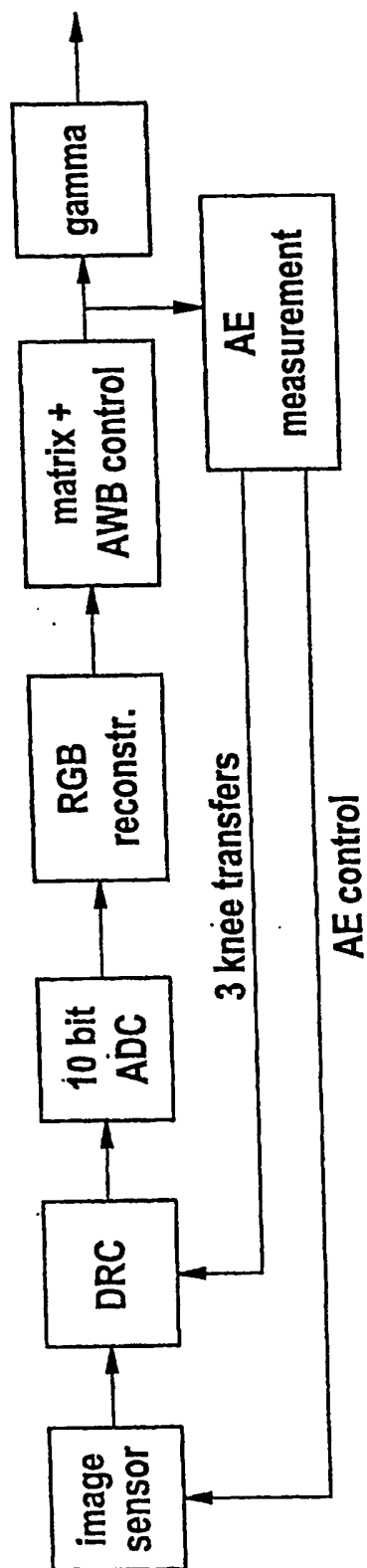


FIG.10

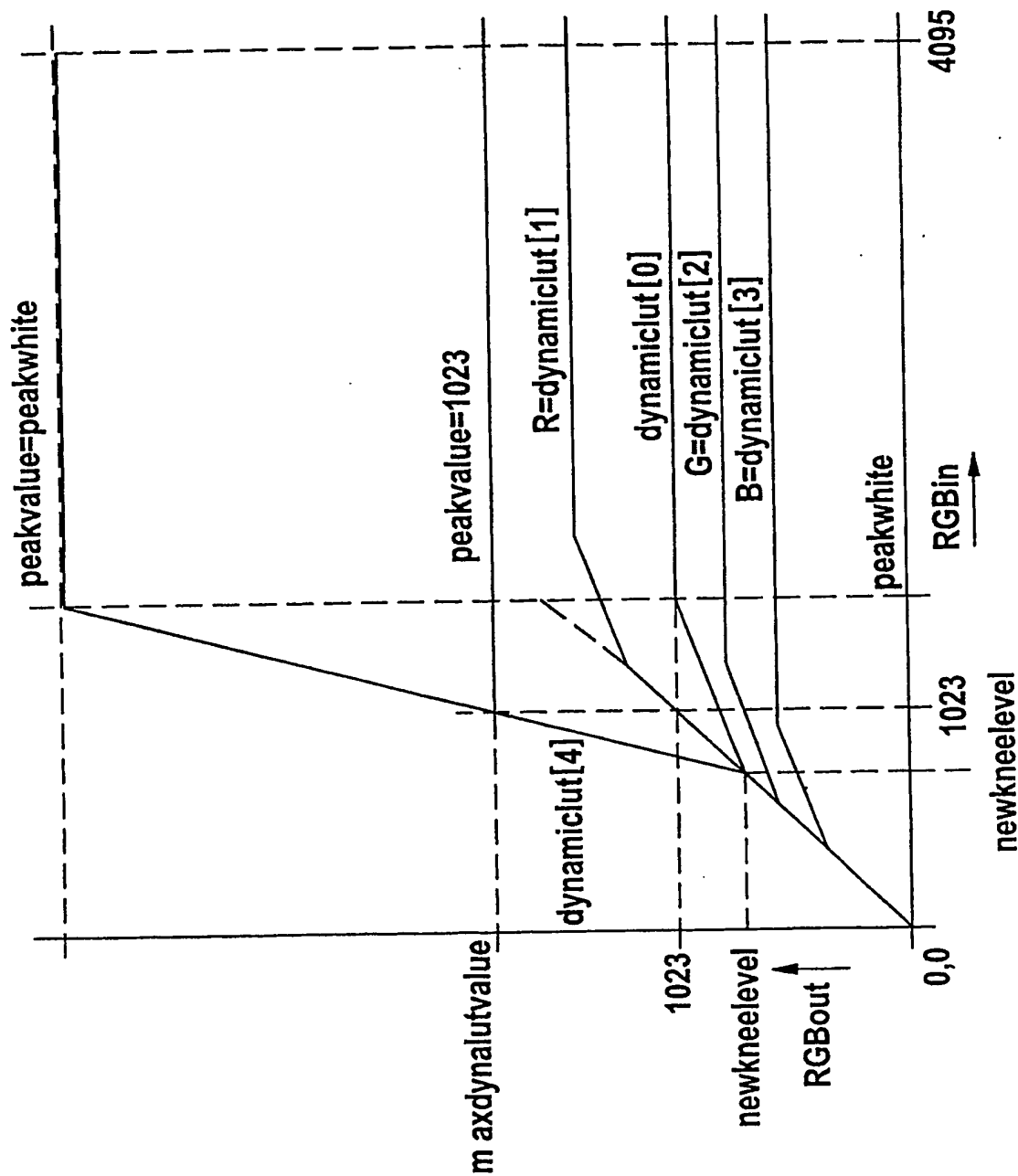


FIG.11

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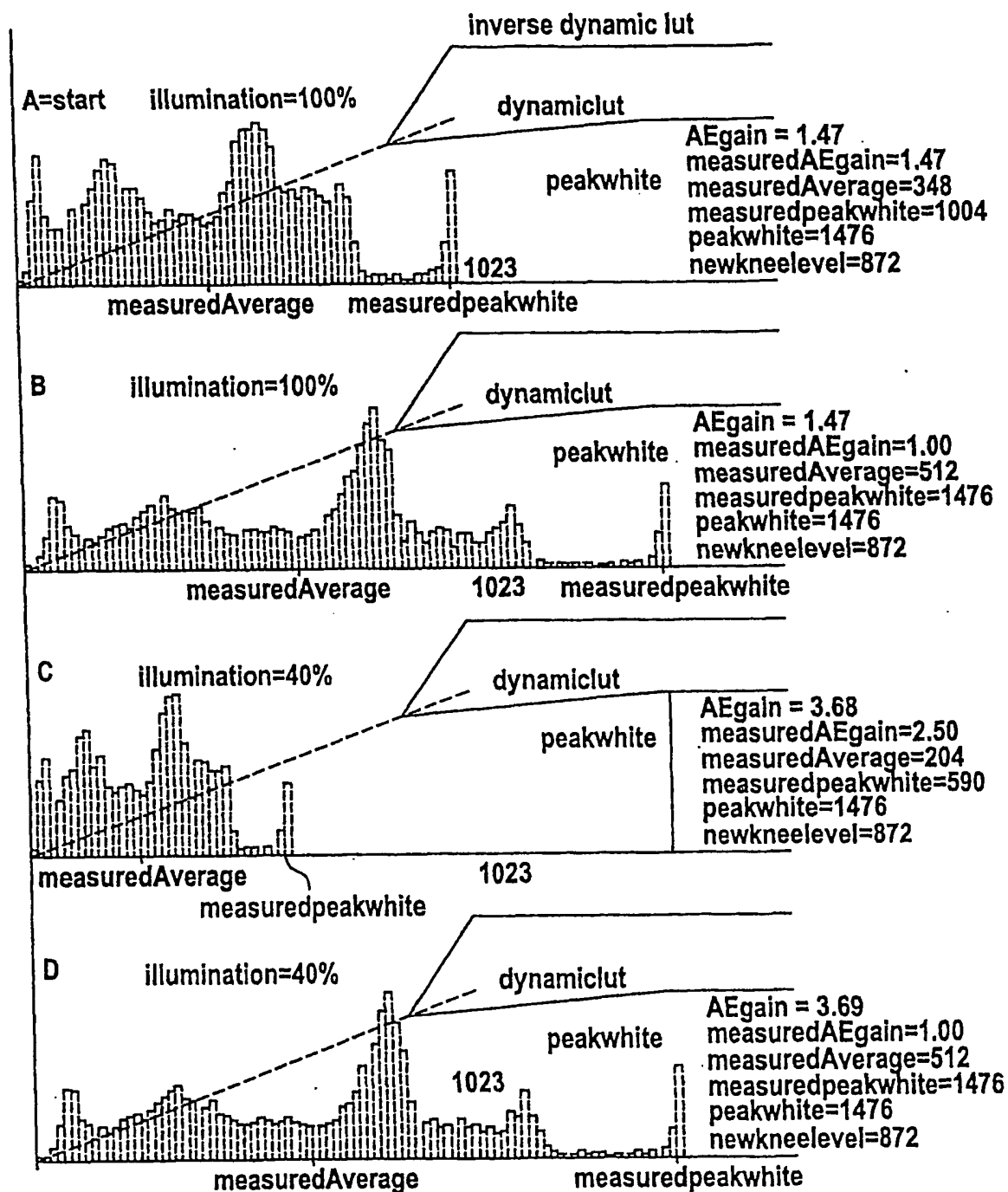


FIG.12

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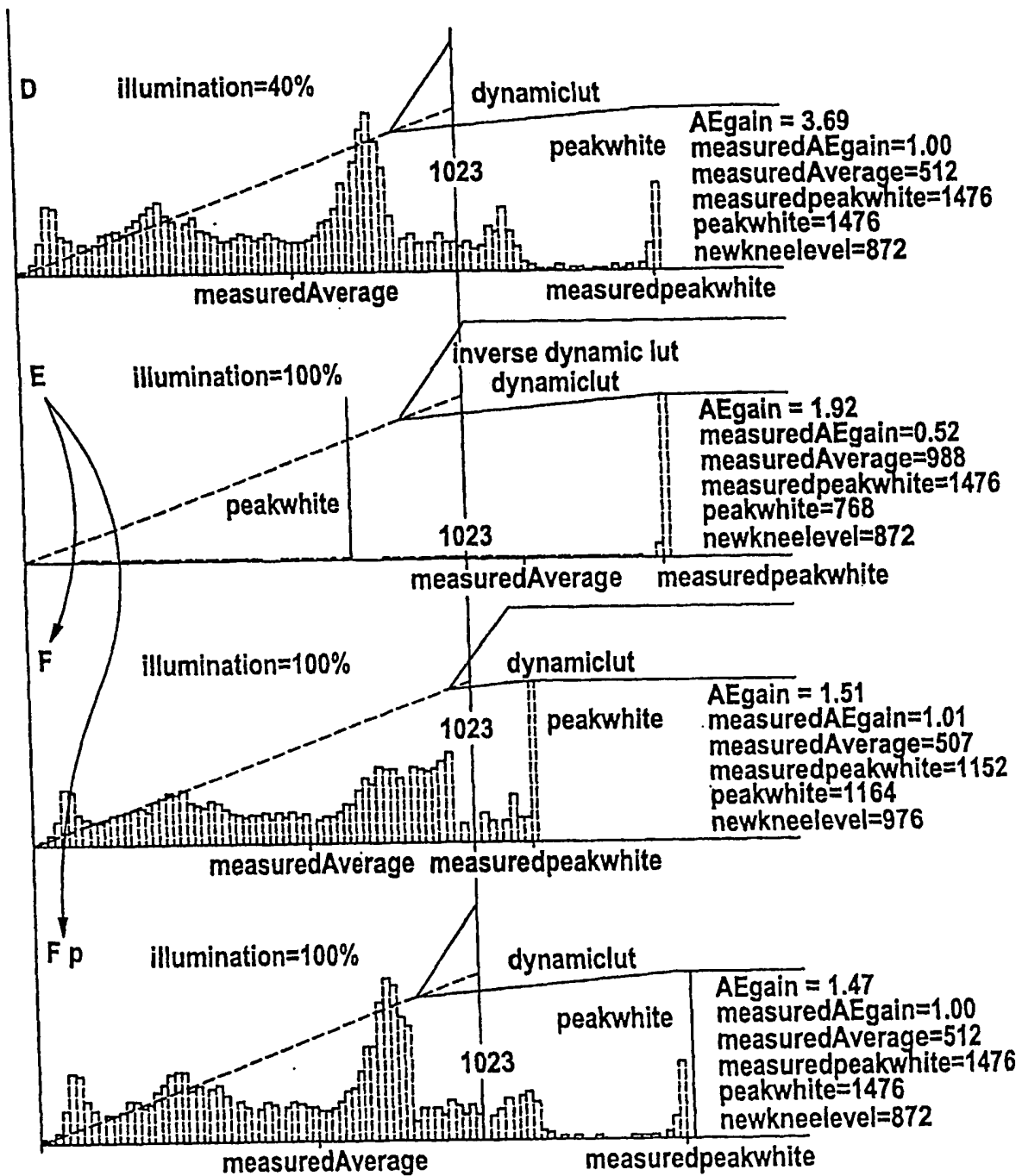
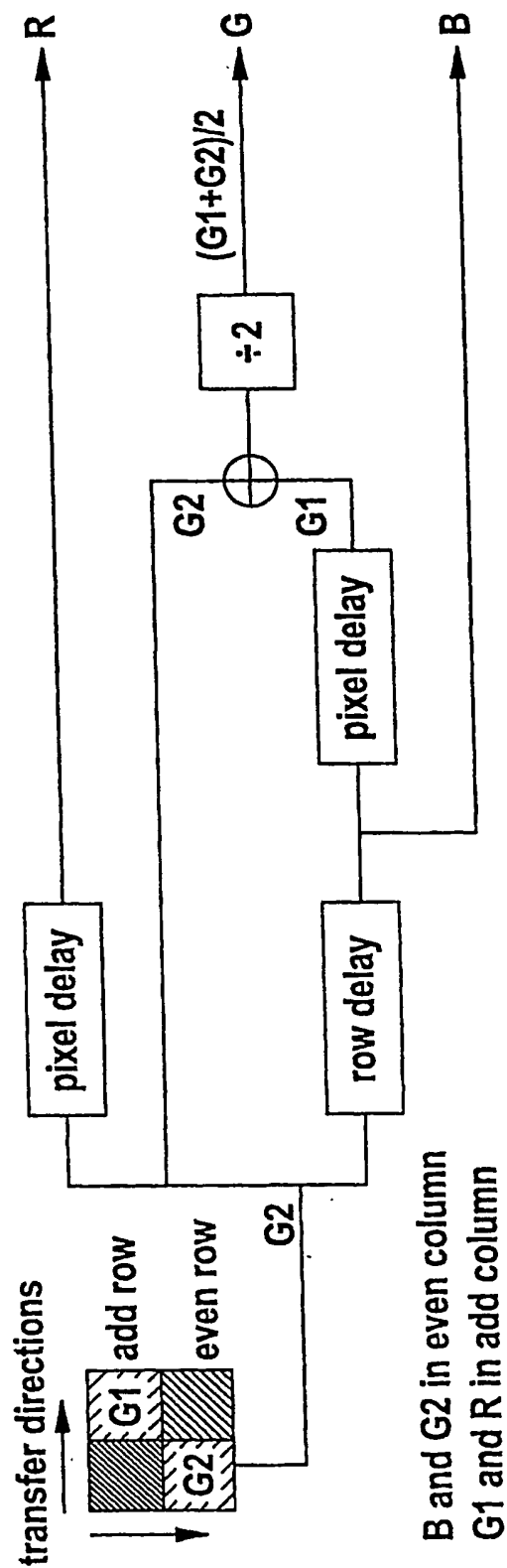


FIG.13



**FIG. 14**

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input								
v \ h	1	2	3	4	5	6	7	8
1	B	G	B	G	B	G	B	G
2	G	R	G	R	G	R	G	R
3	B	G	B	G	B	G	B	G
4	G	R	G	R	G	R	G	R
5	B	G	B	G	B	G	B	G
6	G	R	G	R	G	R	G	R
7	B	G	B	G	B	G	B	G
8	G	R	G	R	G	R	G	R

output				
v \ h	1	2	3	4
1				
2	RGB	RGB	RGB	RGB
3				
4	RGB	RGB	RGB	RGB
5				
6	RGB	RGB	RGB	RGB
7				
8	RGB	RGB	RGB	RGB

FIG.15

input								
v \ h	1	2	3	4	5	6	7	8
1	B	G	B	G	B	G	B	G
2	G	R	G	R	G	R	G	R
3	B	G	B	G	B	G	B	G
4	G	R	G	R	G	R	G	R
5	B	G	B	G	B	G	B	G
6	G	R	G	R	G	R	G	R
7	B	G	B	G	B	G	B	G
8	G	R	G	R	G	R	G	R

output				
v \ h	1	2	3	4
1	RGB	RGB	RGB	RGB
2	RGB	RGB	RGB	RGB
3	RGB	RGB	RGB	RGB
4	RGB	RGB	RGB	RGB

FIG.16